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-	123	jtag and id\$2 and (reconfigur\$7 or configur\$7) and software	USPAT	2002/05/01 15:06
-	88	jtag and (device with id\$2) and (reconfigur\$7 or configur\$7) and software	USPAT	2002/05/01 15:06
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-	1870	(boundary adj2 scan or test adj access adj port or tap) same (id or identification or code)	USPAT	2002/07/02 12:48
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-	253	((boundary adj2 scan or test adj access adj port or tap) same (id or identification or code)) same voltage	USPAT	2002/07/02 12:48
-	9	((boundary adj2 scan or test adj access adj port or tap) same (id or identification or code)) same voltage) same pin\$2	USPAT	2002/07/02 15:25
-	1	(tap with ((multiple or plural) with device with id))	USPAT	2002/07/02 15:27
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-	215	(plural or multiple) with id with code	USPAT	2002/07/02 15:30
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1 An instruction fetch unit applicable to 32-bit and 16-bit instruction s together*Li Lin; Qi Jiayue*

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3 A register-controlled symmetrical DLL for double-data-rate DRAM*Feng Lin; Miller, J.; Schoenfeld, A.; Ma, M.; Baker, R.J.*

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